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## APPLICANTS

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\*\* CONTINUING DATA \*\*\*\*\* *W*\*\* FOREIGN APPLICATIONS \*\*\*\*\* *W*

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED \*\* SMALL ENTITY \*\*

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Foreign Priority claimed	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged	<i>W</i> Examiner's Signature	<i>W</i> Initials	NV	10	9

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## TITLE

Method and apparatus for accelerating the verification of application specific integrated circuit designs

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